

714	724	Subclass
714	Class	ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

SB	O.I.P.E.	PATENT DATE
SCANNED 10/1/03	Q.A. RC	

APPLICATION NO. 09/670855	CONT/PRIOR	CLASS 714	SUBCLASS 724	ART UNIT 2133	EXAMINER Abraham Goncalves E Saad
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APPLICANTS

Deterministic testing of edge-triggered logic
CIRCUITSPTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
714	724	324	73.1		
INTERNATIONAL CLASSIFICATION		326	16		
G01R	31/28				
H03K	19/00				
G01R	27/28				
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets	Drwg.	Figs. Drwg.	Print Fig.	Total Claims
	3	7	3	1	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	E Saad Abraham (Assistant Examiner) 10/17/03 (Date)			NOTICE OF ALLOWANCE MAILED 10/21/103	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	PHUNG M. CHUNG PRIMARY EXAMINER 10/17/03 (Primary Examiner) (Date)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	V. Jones (Legal Instruments Examiner) 10/22/03 (Date)			Amount Due	Date Paid
ISSUE BATCH NUMBER					

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